TO\_WTA\_SEL

OPERAND

RESET

(RST)

DECODER

INCREMENT

(INC)

DECODER

WRITE TO REGISTER

(WTR)

DECODER

PC

IR

Z

AC

T2

T2

T1

T0

CURR

SUM

AVAL

COL

ROW

AR

DR

TR

N

M

P

WRITE TO A BUS

(WTA)

MUL\_en

ADDV\_en

XOR\_en

ADD\_en

MULV\_en

|  |  |  |
| --- | --- | --- |
| Selection Bits for WTA and WTR | Selection Bits for RST | Selection Bits for INC |
| 00000 – N  00001 – M  00010 – P  00011 – M  ..  ..  ..  01111 – AC | 0001-ROW  0010-COL  0100-CURR  1000-SUM | 001-ROW  010-COL  100-CURR |